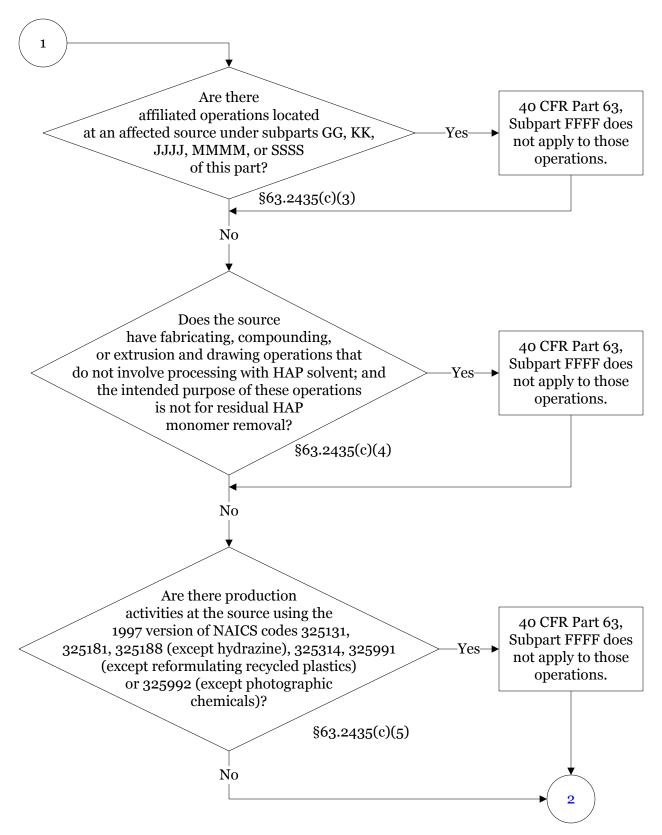
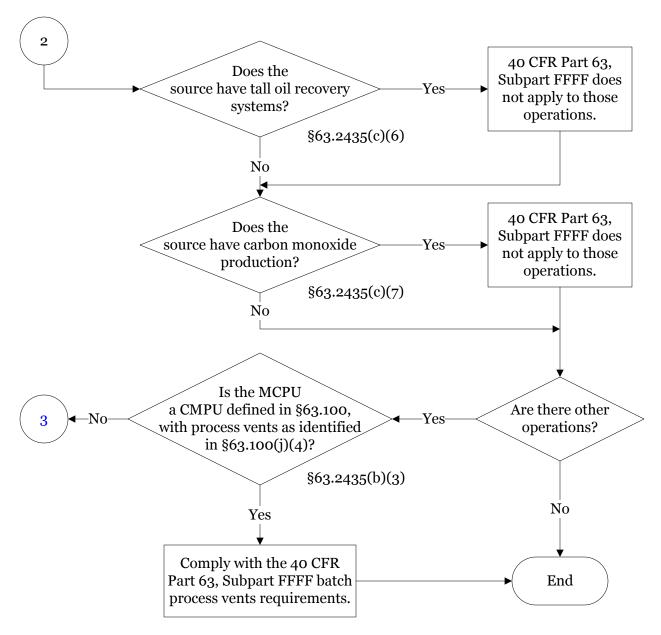


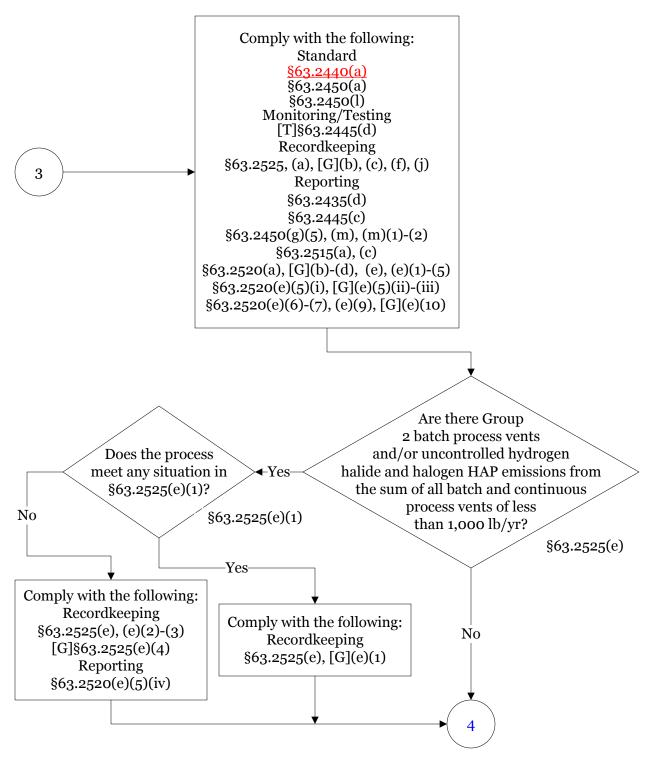
#### 40 CFR 63, Subpart FFFF: MCPU Process (2 of 12)



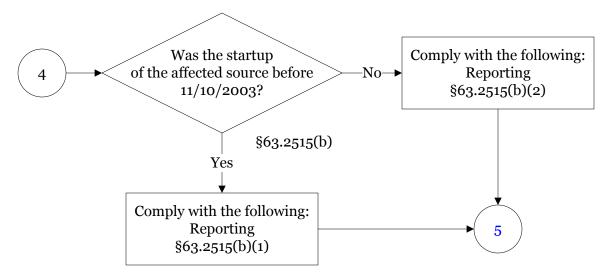
## 40 CFR 63, Subpart FFFF: MCPU Process (3 of 12)



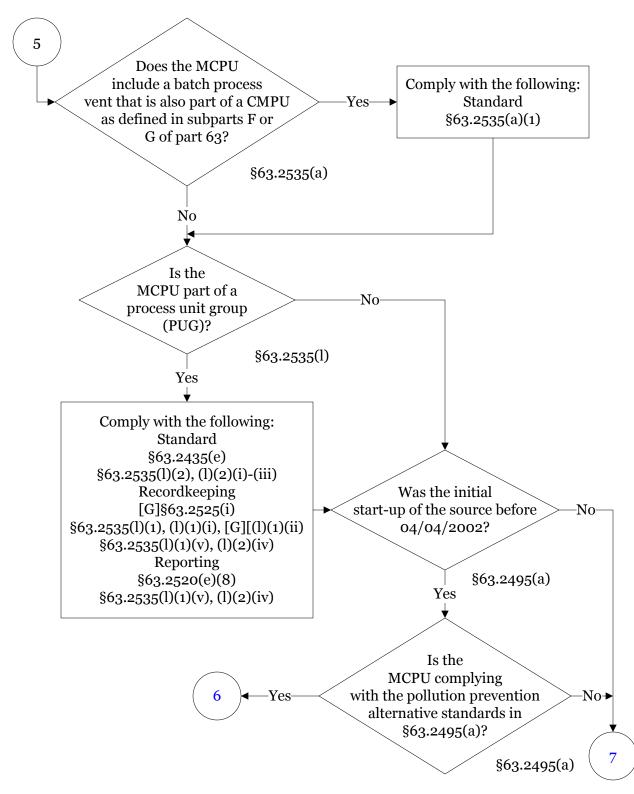
# 40 CFR 63, Subpart FFFF: MCPU Process (4 of 12)



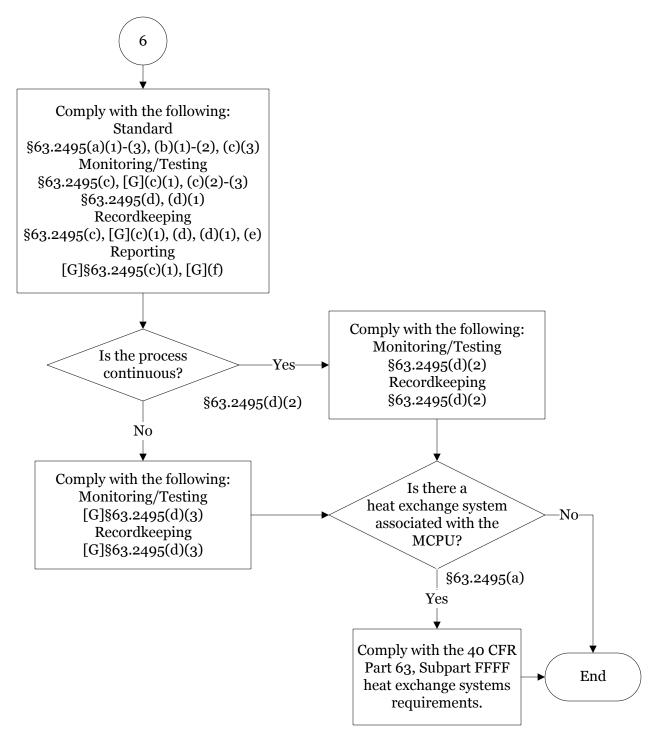
# 40 CFR 63, Subpart FFFF: MCPU Process (5 of 12)



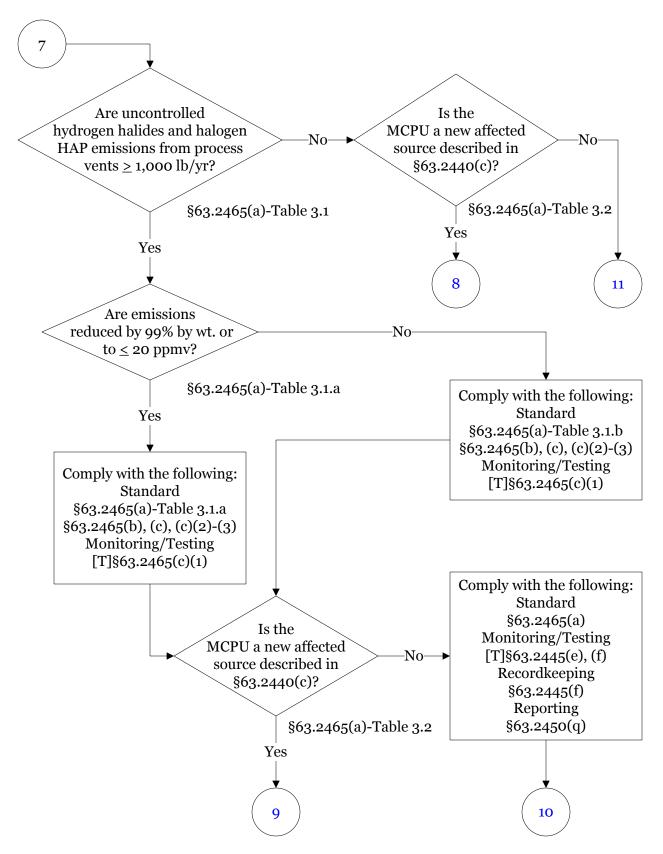
## 40 CFR 63, Subpart FFFF: MCPU Process (6 of 12)



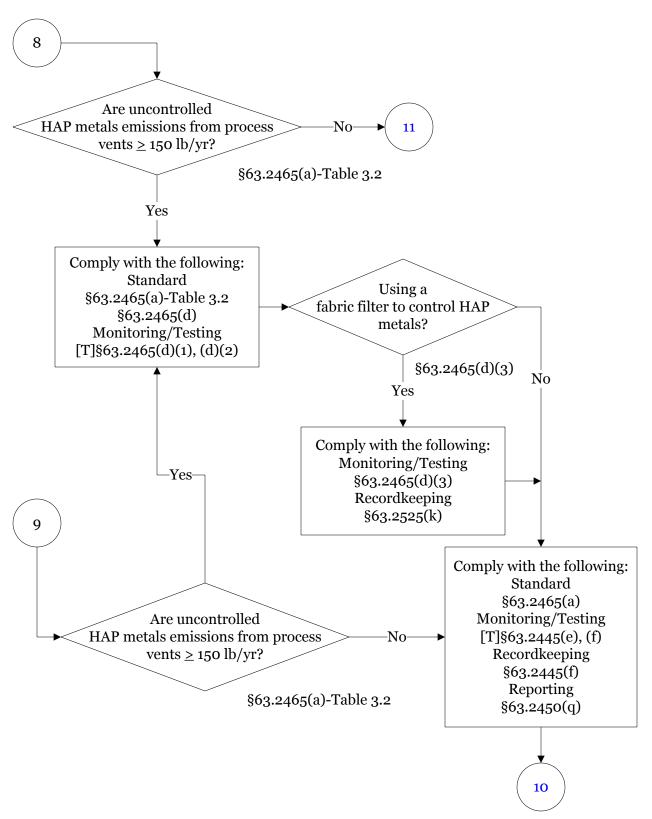
# 40 CFR 63, Subpart FFFF: MCPU Process (7 of 12)



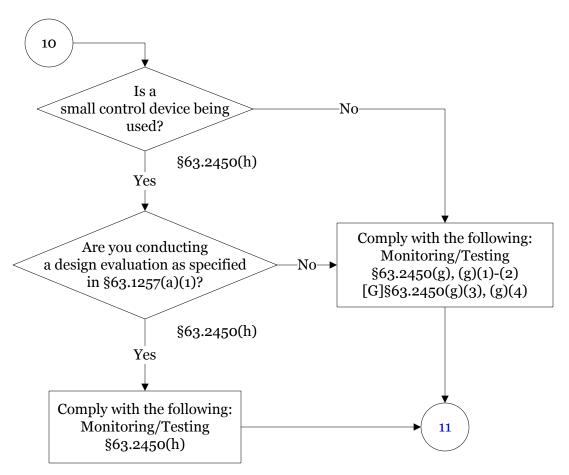
#### 40 CFR 63, Subpart FFFF: MCPU Process (8 of 12)



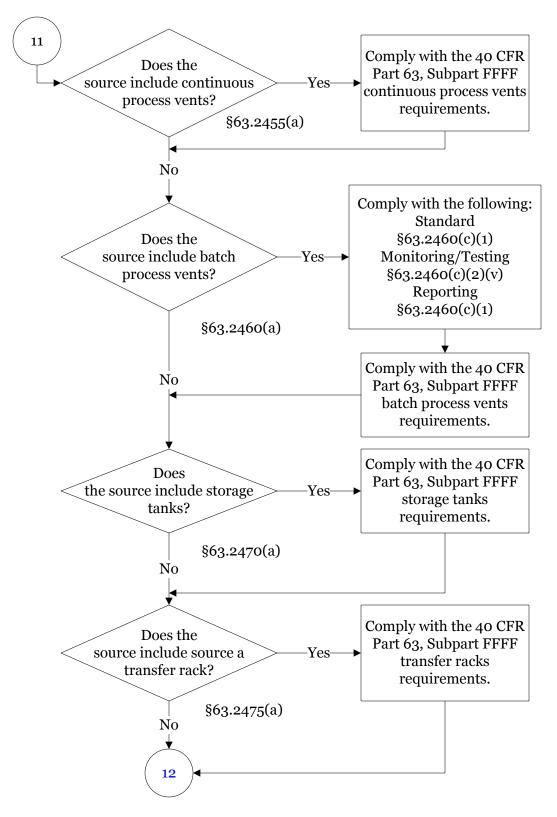
#### 40 CFR 63, Subpart FFFF: MCPU Process (9 of 12)



## 40 CFR 63, Subpart FFFF: MCPU Process (10 of 12)



## 40 CFR 63, Subpart FFFF: MCPU Process (11 of 12)



## 40 CFR 63, Subpart FFFF: MCPU Process (12 of 12)

